

M88DDR4RCD01

Gen1 DDR4 RCD for RDIMM / LRDIMM

General Description

M88DDR4RCD01 is a Gen1 DDR4 Registering Clock Driver (RCD) for driving address and control nets on DDR4 RDIMM or LRDIMM applications. The RCD is fully compliant with the JEDEC DDR4RCD01 specification and can be used as a central buffer on an RDIMM or NVDIMM to enable higher capacity for the memory systems.

Applications

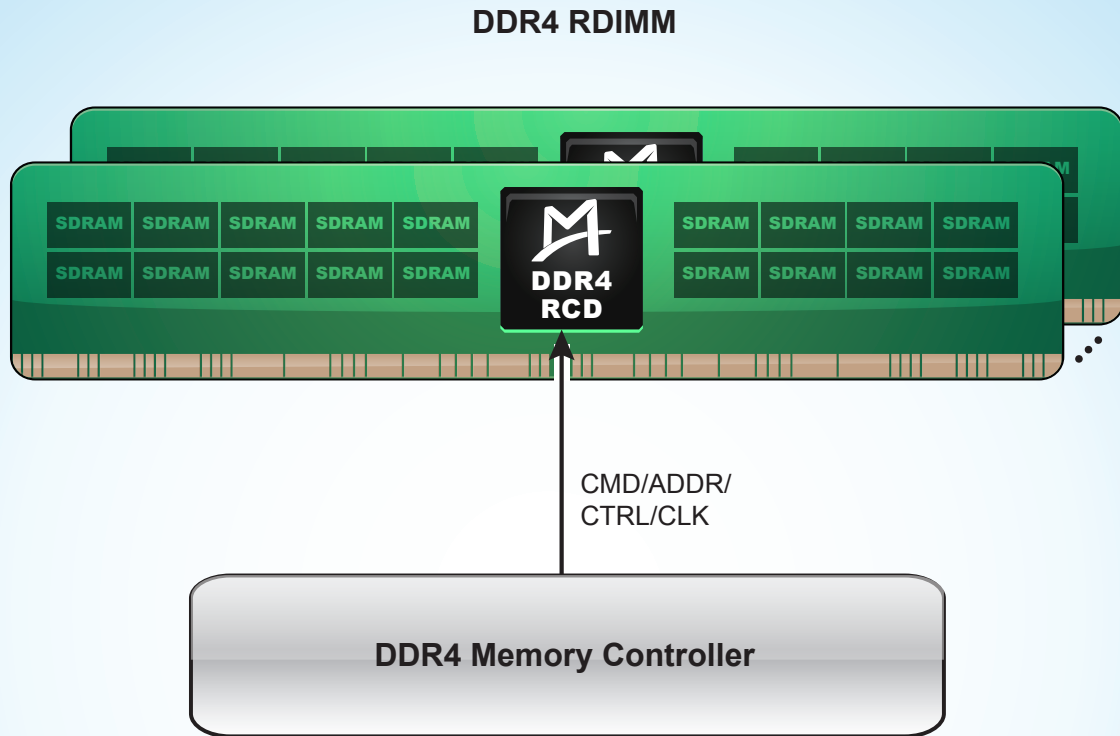
- DDR4 RDIMM, LRDIMM

Feature List

- Fully compliant with JEDEC DDR4RCD01 specification
- Speed up to DDR4-2400
- 1.2 V VDD voltage
- 32-bit 1:2 registering buffer for address and control signals
- Integrated PLL clock driver distributing one differential clock pair to five differential pairs
- Dual and Quad chip selects
- Parity checking on command and address inputs
- Output characteristics configurable through control registers
- Constant propagation delay with VT variations
- CA bus training mode support to align the incoming CA and CTRL signals optimally
- I²C interface supported
- Different power saving modes such as S3 low power mode, CK Stop mode, etc.
- Ultra low power consumption
- Green package: 253-ball Fine-Pitch Grid Array (FBGA)



Application Diagram



* Here shows the RDIMM application as an example.

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