

M88DR4RCD02P & M88D(N)R4DB02P

Gen2 Plus DDR4 RCD & DB for LRDIMM / NVDIMM

General Description

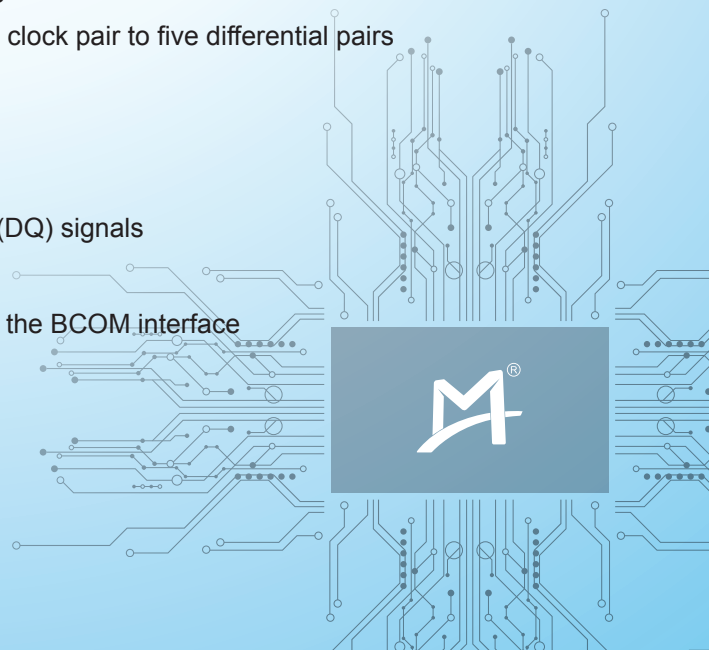
M88DR4RCD02P is a Gen2 Plus Registering Clock Driver (RCD). M88DR4DB02P/M88NR4DB02P is Gen2 Plus Data Buffer (DB). These chips are fully compliant with JEDEC DDR4 specifications. An RCD can be used in conjunction with nine DBs on an LRDIMM or NVDIMM to provide advanced memory solutions with higher capacity, higher speed and lower power for DDR4 server platforms.

Applications

- DDR4 LRDIMM (M88DR4RCD02P + M88DR4DB02P)
- DDR4 NVDIMM (M88DR4RCD02P + M88NR4DB02P)

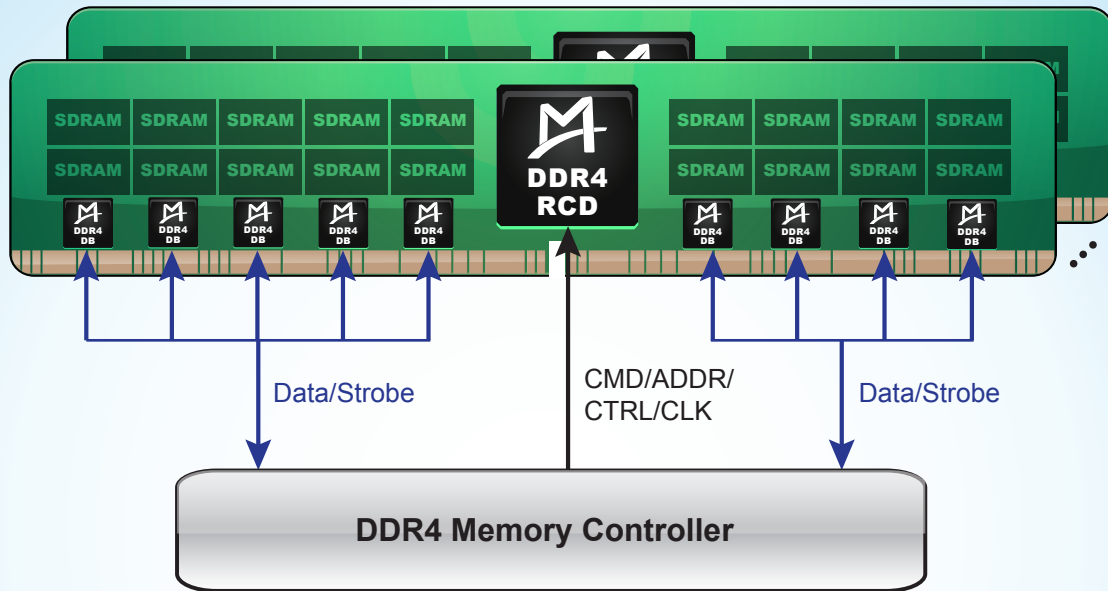
Feature List

- Fully compliant with JEDEC DDR4RCD02 and DDR4DB02 specifications
- Speed up to DDR4-3200
- 1.2 V VDD voltage
- M88DR4RCD02P:
 - 32-bit 1:2 registering buffer for address and control signals
 - Integrated PLL clock driver distributing one differential clock pair to five differential pairs
 - Parity checking on command and address inputs
 - Green package: 253-ball FBGA
- M88DR4DB02P/M88NR4DB02P:
 - Bidirectional retiming and 1:1 redriving of DDR4 data (DQ) signals
 - Only x4 DRAM devices supported
 - Parity error checking and sequence error checking on the BCOM interface
 - Green package: 53-ball FBGA for M88DR4DB02P
56-ball FBGA for M88NR4DB02P



Application Diagram

DDR4 LRDIMM



* Here shows the LRDIMM application as an example.

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