

M88DR4RCD02P

Gen2 Plus DDR4 RCD for RDIMM / LRDIMM / NVDIMM

General Description

M88DR4RCD02P is a Gen2 Plus DDR4 Registering Clock Driver (RCD) for driving address and control nets on DDR4 RDIMM, LRDIMM or NVDIMM applications. The RCD is fully compliant with the JEDEC DDR4RCD02 specification and can be used as a central buffer on an RDIMM or NVDIMM to enable higher capacity for the memory systems.

Applications

DDR4 RDIMM, LRDIMM, NVDIMM

Feature List

- Fully compliant with JEDEC DDR4RCD02 specification
- Support up to DDR4-3200
- 1.2 V VDD voltage
- 32-bit 1:2 registering buffer for address and control signals
- Integrated PLL clock driver distributing one differential clock pair to five differential pairs
- Dual and Quad chip selects
- · Parity checking on command and address inputs
- · Output characteristics configurable through control registers
- Constant propagation delay with VT variations
- CA bus training mode support to align the incoming CA and CTRL signals optimally
- I2C interface support
- Different power saving modes such as S3 low power mode, CK Stop mode, etc.
- Green package: 253-ball Fine-Pitch Ball Grid Array (FBGA)

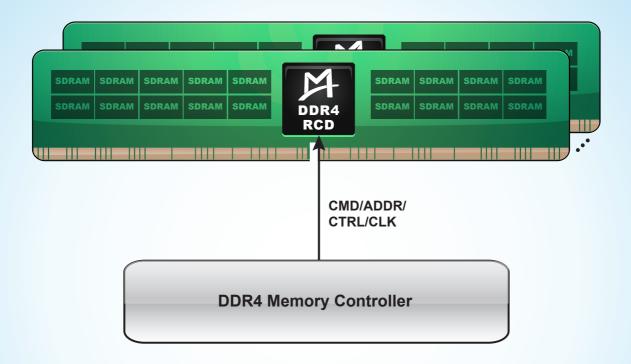


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Application Diagram

DDR4 RDIMM



* Here shows the RDIMM application as an example.

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