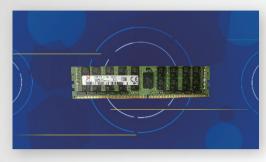


Montage Technology HSDIMM[®] Product Brief



Based on JEDEC DDR4 LRDIMM architecture, HSDIMM® is designed with Montage proprietary Mont-ICMT® technology to provide real-time tracing on both command/address and transaction data and recording of such traced data for server platform's data security. As a main component of Montage Jintide® server platform, HSDIMM® works together with the DSC (Dynamic Security Check) function built in Jintide® CPU to implement dynamic security check at silicon level for data center servers that require a high level of data security.

HSDIMM[®] Features

Integrated Mont-ICMT[®] (Inspection and Control on Memory Traffic) function and dedicated DCI interface

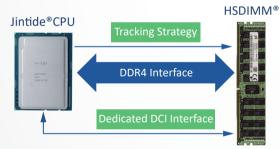
Work with the integrated DSC function in the Jintide[®] CPU to implement dynamic security check for high-end servers

Compliant with JEDEC standard DDR4 LRDIMM, operation speed up to 2666 MT/s

Provide silicon-level security function with little impact on the DIMM performance

MONT-ICMT® Features

- 01 Monitor/trace the memory access command from CPU and the transaction data between CPU and DIMM at real time
- 02 Record the traced data and command/address information as required during the tracing process
- 3 The recorded data and command information can be transferred to Jintide[®] CPU through the dedicated DCI interface for dynamic security check purposes, such as instruction replay, security analysis, etc.



Key Markets

Cloud computing and big data centers used for various industries, such as:



Application Scenario



As one of the critical parts of Jintide[®] CPU, HSDIMM[®] provides real-time inspection on memory data transactions and control of tracing action.



HSDIMM[®] can upload recorded memory data for customers to conduct real-time security analysis.

Montage Technology Address: 16/F, 181 Caobao Road, Montage Circle, Xuhui District, Shanghai 200233, China Phone: +86-21-5467-9038 Fax: +86-21-5426-3132 Sales Contact: globalsales@montage-tech.com www.montage-tech.com



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